

**ABSTRACT**

A clock down sensor mainly comprises a converter and a low-pass  
5 filter. The converter is used to convert an input signal from a PECL  
(Positive Emitter Coupling Logic) signal to a TTL (Transistor  
Transistor Logic) signal, the low pass filter is used to obtain a DC  
(Direct Current) level of the TTL signal. Thereby, the sensor can  
10 judge whether the clock signal is terminated according to the potential  
of the output signal in order to emit a warning so that a breakdown  
elimination inquiry can be done or automatic breakdown elimination  
can be processed earlier.